

## UTTARAKHAND BOARD OF TECHNICAL EDUCATION JOINT ENTRANCE EXAMINATION AND TRAINING, RESEARCH DEVELOPMENT CELL, DEHRADUN STUDY AND EVALUATION SCHEME FOR DIPLOMA PROGRAMME

## BRANCH NAME - ELECTRONICS & COMMUNICATION ENGINEERING

SEMESTER - III

Subject Code	Subject	L	Т	P	T O	EVALUATION SCHEME							
			.4		T	Internal Exteri				ernal	rnal		Credit
					Theory Practical		Theory		Practical		Marks	Point	
		Period/Weeks				Max Marks	Max Marks	Max Marks	Hrs.	Max Marks	Hrs.	1	
093004	Basics of Electrical Engineering and Machinery	3	1	4	8	35	30	75	2.5	50	3.0	190	6
093001	Analog Electronics	4	1	4	9	35	30	75	2.5	50	3.0	190	6
093002	Digital Electronics	4	1	4	9	35	30	75	2.5	50	3.0	190	6
093005	Communication Engineering	4	1	4	9	35	30	75	2.5	50	3.0	190	5
093003	Basics of C Programming	4	1	4	9	35	30	75	2.5	50	3.0	190	5
093052	Industrial Exposure (Assessment at Inst. Level)+	-	) <b>-</b> )	-	- ,	10	25	100	9-/	-	-	25	1
013054	General Proficiency#	-	-	4	4	/-	25	/ =		-	-	25	1
Total		19	5	24	48	175	200	375	/-	250	-	1000	30

<sup>#</sup> General Proficiency will comprise of various co-curricular activities like games, hobby clubs, seminars, declamation contests, extension lectures, NCC, NSS and cultural activities and discipline etc.

Note: 1- Each period will be 50 minutes. 2- Each session will be of 16 weeks. 3- Effective teaching will be at least 12.5 weeks. + Industrial visit.

<sup>+</sup> Industrial visit compulsory at minimum 2 Industries or Department.